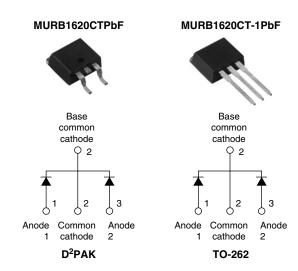




Vishay High Power Products

## Ultrafast Rectifier, 2 x 8 A FRED Pt<sup>TM</sup>



PRODUCT SUMMARY				
t <sub>rr</sub>	25 ns			
I <sub>F(AV)</sub>	2 x 8 A			
V <sub>R</sub>	200 V			

#### **FEATURES**

- · Ultrafast recovery time
- · Low forward voltage drop
- · Low leakage current
- 175 °C operating junction temperature
- Lead (Pb)-free ("PbF" suffix)
- · Designed and qualified for AEC Q101 level

#### **DESCRIPTION/APPLICATIONS**

MUR.. series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Peak repetitive reverse voltage	$V_{RRM}$		200	٧	
Average restified forward surrent	I <sub>F(AV)</sub>		8.0		
Average rectified forward current total device		Rated V <sub>R</sub> , T <sub>C</sub> = 150 °C	16		
Non-repetitive peak surge current per leg	I <sub>FSM</sub>		100	Α	
Peak repetitive forward current per leg	I <sub>FM</sub>	Rated V <sub>R</sub> , square wave, 20 kHz, T <sub>C</sub> = 150 °C	16		
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		- 65 to 175	°C	

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	Ι <sub>R</sub> = 100 μΑ	200	-	-		
Forward voltage V <sub>F</sub>	.,,	I <sub>F</sub> = 8 A	=	-	0.975	V	
	I <sub>F</sub> = 8 A, T <sub>J</sub> = 150 °C	=	-	0.895			
Reverse leakage current I <sub>R</sub>	V <sub>R</sub> = V <sub>R</sub> rated	=	-	5			
	IR.	T <sub>J</sub> = 150 °C, V <sub>R</sub> = V <sub>R</sub> rated	=	-	250	μΑ	
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 200 V	=	25	-	pF	
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8.0	-	nΗ	

<sup>\*</sup> Pb containing terminations are not RoHS compliant, exemptions may apply

Document Number: 94519 Revision: 05-Sep-08

Vishay High Power Products

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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time t <sub>rr</sub>		$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		1	ı	35	ns
		I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1.0 A, I <sub>REC</sub> = 0.25 A		-	-	25	
	L <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	- 20 -		
		T <sub>J</sub> = 125 °C	I <sub>F</sub> = 8 A dI <sub>F</sub> /dt = 200 A/μs V <sub>R</sub> = 160 V	-	34	-	
Peak recovery current I <sub>RRM</sub>	_	T <sub>J</sub> = 25 °C		-	1.7	-	Α
	'RRM	T <sub>J</sub> = 125 °C		-	4.2	-	^
Reverse recovery charge Q <sub>rr</sub>	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	23	-	nC
		T <sub>J</sub> = 125 °C		=	75	=	110

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 65	-	175	°C	
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	-	3.0		
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>		-	-	50	°C/W	
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.5	-		
Weight			-	2.0	-	g	
			-	0.07	-	OZ.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)	
Marking davisa		Case style D <sup>2</sup> PAK		MURB1620CT			
Marking device		Case style TO-262		MURB1620CT-1			

Document Number: 94519 Revision: 05-Sep-08



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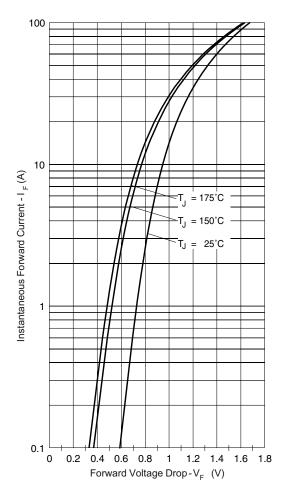


Fig. 1 - Typical Forward Voltage Drop Characteristics

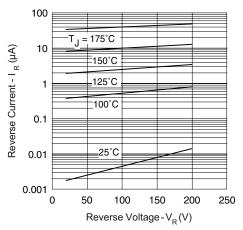


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

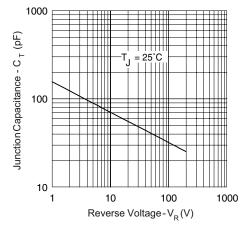


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

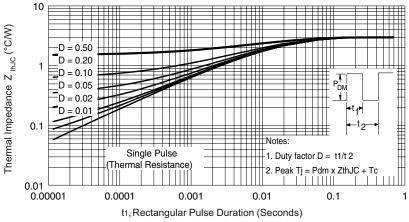


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

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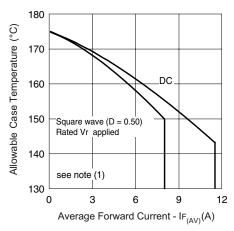


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

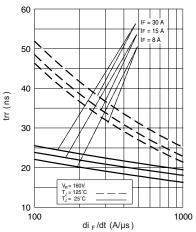


Fig. 7 - Typical Reverse Recovery Time vs.  $dI_F/dt$ 

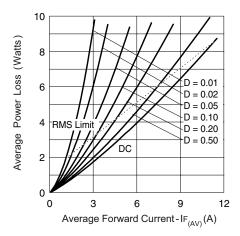


Fig. 6 - Forward Power Loss Characteristics

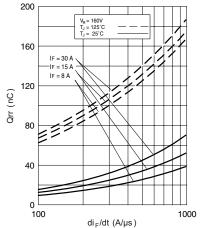


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

#### Note

 $\begin{array}{l} \text{(1)} \ \ \text{Formula used:} \ T_C = T_J - (Pd + Pd_{REV}) \ x \ R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \ x \ V_{FM} \ \text{at} \ (I_{F(AV)}/D) \ (\text{see fig. 6}); \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \ x \ I_{R} \ (1 - D); \ I_{R} \ \text{at} \ V_{R1} = \text{Rated} \ V_{R} \\ \end{array}$ 



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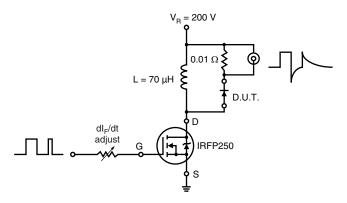
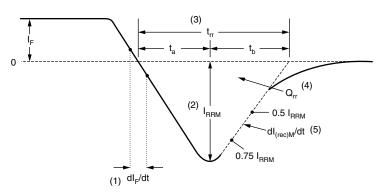


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dI<sub>F</sub>/dt rate of change of current through zero crossing
- (2) I<sub>RRM</sub> peak reverse recovery current
- (3)  $\rm t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $\rm I_F$  to point where a line passing through 0.75  $\rm I_{RRM}$  and 0.50  $\rm I_{RRM}$  extrapolated to zero current.
- (4)  $\mathbf{Q}_{rr}$  area under curve defined by  $\mathbf{t}_{rr}$  and  $\mathbf{I}_{RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5)  $dI_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$ 

Fig. 10 - Reverse Recovery Waveform and Definitions

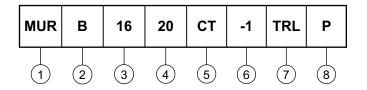
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#### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Ultrafast MUR series

3 - Current rating (16 = 16 A)

4 - Voltage rating (20 = 200 V)

5 - CT = Center tap (dual)

None = D<sup>2</sup>PAK

• -1 = TO-262

7 - • None = Tube (50 pieces)

• TRL = Tape and reel (left oriented, for D<sup>2</sup>PAK only)

• TRR = Tape and reel (right oriented, for D<sup>2</sup>PAK only)

8 - • None = Standard production

• PbF = Lead (Pb)-free (for TO-262 and D<sup>2</sup>PAK tube)

• P = Lead (Pb)-free (for D<sup>2</sup>PAK TRR and TRL)

LINKS TO RELATED DOCUMENTS				
Dimensions	http://www.vishay.com/doc?95014			
Part marking information	http://www.vishay.com/doc?95008			
Packaging information	http://www.vishay.com/doc?95032			

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